

## EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L2	2	"5982990".pn.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/23 13:15
L3	9	colorsmart	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/23 13:17
L4	2	"5402245".pn.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/23 14:57
L5	2	"6697520".pn.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/23 14:58
L6	5	("3893166"   "4275413"   "5504821"   "6289138"   "6466333").PN. OR ("6697520").URPN.	US-PGPUB; USPAT; USOCR	OR	ON	2006/10/23 16:33
L7	4535	358/1.9.ccls.	US-PGPUB; USPAT; USOCR	OR	ON	2006/10/23 16:33
L8	160	358/522.ccls.	US-PGPUB; USPAT; USOCR	OR	ON	2006/10/23 16:33
L9	259	358/525.ccls.	US-PGPUB; USPAT; USOCR	OR	ON	2006/10/23 16:34
L10	571	382/300.ccls.	US-PGPUB; USPAT; USOCR	OR	ON	2006/10/23 16:34
L11	1386	382/167.ccls.	US-PGPUB; USPAT; USOCR	OR	ON	2006/10/23 16:34
L12	250	358/524.ccls.	US-PGPUB; USPAT; USOCR	OR	ON	2006/10/23 16:34

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L13	574	358/523.ccls.	US-PGPUB; USPAT; USOCR	OR	ON	2006/10/23 16:34
L14	160	358/522.ccls.	US-PGPUB; USPAT; USOCR	OR	ON	2006/10/23 16:34
S1	254	tetrahedral near interpolat\$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/20 12:15
S2	1387	382/167.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/19 15:22
S3	23	S1 and S2	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/19 15:22
S4	254	tetrahedral near interpolat\$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/20 12:15
S5	2	S4 and (size near1 order)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/20 12:16
S6	3	S4 and (sequen\$4 near1 order)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/20 12:18
S7	14	S4 and ((sequen\$4 or rang\$3 or dimension\$3) near1 order)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/20 12:20

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S8	2	"6049400".pn.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/20 12:21
S9	10	"6049400".pn. or "6040926".pn. or "6040925".pn. "6031642".pn. "6028683".pn.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/23 13:01



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## » Key

IEEE JNL IEEE Journal or Magazine

IEE JNL IEE Journal or Magazine

IEEE CNF IEEE Conference Proceeding

IEE CNF IEE Conference Proceeding

IEEE STD IEEE Standard

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 Marcu, G.; Iwata, K.;  
Acoustics, Speech, and Signal Processing, 1994. ICASSP-94., 1994 IEEE International Conference on  
 Volume v, 19-22 April 1994 Page(s):V/101 - V/104 vol.5  
 Digital Object Identifier 10.1109/ICASSP.1994.389555  
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- ☐ 2. **Color conversions using maximum entropy estimation**  
 Gupta, M.R.; Gray, R.M.;  
Image Processing, 2001. Proceedings. 2001 International Conference on  
 Volume 1, 7-10 Oct. 2001 Page(s):118 - 121 vol.1  
 Digital Object Identifier 10.1109/ICIP.2001.958967  
[AbstractPlus](#) | Full Text: [PDF](#)(304 KB) IEEE CNF  
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- ☐ 3. **Model-based color halftoning using direct binary search**  
 Agar, A.U.; Allebach, J.P.;  
Image Processing, IEEE Transactions on  
 Volume 14, Issue 12, Dec. 2005 Page(s):1945 - 1959  
 Digital Object Identifier 10.1109/TIP.2005.859380  
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1994. (INZZ) Designing the color palette for textile material printing process.

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1993. (INZZ) Colorimetric calibration in electronic imaging devices using a look- up-table model and interpolations.

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# PALM INTRANET

## Inventor Name Search Result

Your Search was:

Last Name = HUNG

First Name = CHING-YU

Application#	Patent#	Status	Date Filed	Title	Inventor Name
<a href="#">09001050</a>	<a href="#">6327601</a>	150	12/30/1997	LINEAR TRANSFORM SYSTEM FOR DECODING VIDEO DATA	HUNG, CHING-YU
<a href="#">09200348</a>	<a href="#">6263470</a>	150	11/25/1998	EFFICIENT LOOK-UP TABLE METHODS FOR REED-SOLOMON DECODING	HUNG, CHING-YU
<a href="#">09244674</a>	<a href="#">6256724</a>	150	02/04/1999	DIGITAL SIGNAL PROCESSOR WITH EFFICIENTLY CONNECTABLE HARDWARE CO-PROCESSOR	HUNG, CHING-YU
<a href="#">09244973</a>	<a href="#">6298366</a>	150	02/04/1999	RECONFIGURABLE MULTIPLY-ACCUMULATE HARDWARE CO-PROCESSOR UNIT	HUNG, CHING-YU
<a href="#">09411124</a>	<a href="#">6526430</a>	150	10/04/1999	RECONFIGURABLE SIMD COPROCESSOR ARCHITECTURE FOR SUM OF ABSOLUTE DIFFERENCES AND SYMMETRIC FILTERING (SCALABLEMAC ENGINE FOR IMAGE PROCESSING)	HUNG, CHING-YU
<a href="#">09475928</a>	<a href="#">6530010</a>	150	12/30/1999	MULTIPLEXER RECONFIGURABLE IMAGE PROCESSING PERIPHERAL HAVING FOR LOOP CONTROL	HUNG, CHING-YU
<a href="#">09632543</a>	Not Issued	120	08/04/2000	Digital still camera system and method	HUNG, CHING-YU
<a href="#">09687546</a>	<a href="#">6591230</a>	150	10/13/2000	COPROCESSOR FOR SYNTHESIZING SIGNALS BASED UPON QUADRATIC POLYNOMIAL SINUSOIDS	HUNG, CHING-YU
<a href="#">09742992</a>	<a href="#">6829016</a>	150	12/20/2000	DIGITAL STILL CAMERA SYSTEM AND METHOD	HUNG, CHING-YU

<u>10692154</u>	Not Issued	30	10/22/2003	Tetrahedral interpolation	HUNG, CHING-YU
<u>10692156</u>	Not Issued	30	10/22/2003	Resampling methods	HUNG, CHING-YU
<u>10888701</u>	Not Issued	30	07/09/2004	Reformatter and method	HUNG, CHING-YU
<u>10890509</u>	Not Issued	30	07/13/2004	Video encoding using parallel processors	HUNG, CHING-YU
<u>10936342</u>	Not Issued	30	09/07/2004	Sliding data buffering for image processing	HUNG, CHING-YU
<u>11219925</u>	Not Issued	20	09/06/2005	Digital camera front-end architecture	HUNG, CHING-YU
<u>11220273</u>	Not Issued	30	09/06/2005	Video processing subsystem architecture	HUNG, CHING-YU
<u>11235586</u>	Not Issued	93	09/26/2005	VIDEO CODING WITH CABAC	HUNG, CHING-YU
<u>29150719</u>	D464656	150	11/29/2001	SCANNER	HUNG, CHING-YU
<u>60043393</u>	Not Issued	159	03/27/1997	LINEAR TRANSFORM SYSTEM FOR ENCODING VIDEO DATA	HUNG, CHING-YU
<u>60073595</u>	Not Issued	159	02/03/1998	TABLE LOOKUP METHODS FOR REED-SOLOMON DECODING	HUNG, CHING-YU
<u>60073641</u>	Not Issued	159	02/04/1998	RECONFIGURABLE MULTIPLE MULTIPLY-ACCUMULATE HARDWARE CO-PROCESSOR UNIT	HUNG, CHING-YU
<u>60073668</u>	Not Issued	159	02/04/1998	DIGITAL SIGNAL PROCESSOR WITH EFFICIENTLY CONNECTABLE HARDWARE CO-PROCESSOR	HUNG, CHING-YU
<u>60147207</u>	Not Issued	159	08/04/1999	DIGITAL STILL CAMERA SYSTEM AND METHOD	HUNG, CHING-YU
<u>60165163</u>	Not Issued	159	11/12/1999	COPROCESSOR FOR SYNTHESIZING SIGNALS BASED UPON QUADRATIC POLYNOMIAL SINUSOIDS	HUNG, CHING-YU
<u>60177433</u>	Not Issued	159	01/21/2000	Digital still camera system and method	HUNG, CHING-YU
<u>60381650</u>	Not Issued	159	05/17/2002	Video encoding on parallel processors	HUNG, CHING-YU
<u>60420319</u>	Not Issued	159	10/22/2002	Efficient implementation of tetrahedral interpolation	HUNG, CHING-YU



<a href="#">60487016</a>	Not Issued	159	07/14/2003	Video encoding on parallel processors	HUNG, CHING-YU
<a href="#">60500274</a>	Not Issued	159	09/04/2003	Efficient data buffering scheme for image processing applications	HUNG, CHING-YU
<a href="#">60606944</a>	Not Issued	159	09/03/2004	Video processing front-end architecture	HUNG, CHING-YU
<a href="#">60607380</a>	Not Issued	159	09/03/2004	Video processing subsystem architecture	HUNG, CHING-YU
<a href="#">60612996</a>	Not Issued	159	09/24/2004	Optimization techniques for H.264 video CABAC encode implementation	HUNG, CHING-YU
<a href="#">11354113</a>	Not Issued	41	02/15/2006	Structure of DIY fence	HUNG, CHING-YUN

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## Inventor Name Search Result

Your Search was:

Last Name = TALLA

First Name = DEEPENDRA

Application#	Patent#	Status	Date Filed	Title	Inventor Name
<a href="#">10692154</a>	Not Issued	30	10/22/2003	Tetrahedral interpolation	TALLA, DEEPENDRA
<a href="#">10888701</a>	Not Issued	30	07/09/2004	Reformatter and method	TALLA, DEEPENDRA
<a href="#">10890509</a>	Not Issued	30	07/13/2004	Video encoding using parallel processors	TALLA, DEEPENDRA
<a href="#">11219925</a>	Not Issued	20	09/06/2005	Digital camera front-end architecture	TALLA, DEEPENDRA
<a href="#">11220273</a>	Not Issued	30	09/06/2005	Video processing subsystem architecture	TALLA, DEEPENDRA
<a href="#">60381650</a>	Not Issued	159	05/17/2002	Video encoding on parallel processors	TALLA, DEEPENDRA
<a href="#">60420319</a>	Not Issued	159	10/22/2002	Efficient implementation of tetrahedral interpolation	TALLA, DEEPENDRA
<a href="#">60487016</a>	Not Issued	159	07/14/2003	Video encoding on parallel processors	TALLA, DEEPENDRA
<a href="#">60606944</a>	Not Issued	159	09/03/2004	Video processing front-end architecture	TALLA, DEEPENDRA
<a href="#">60607380</a>	Not Issued	159	09/03/2004	Video processing subsystem architecture	TALLA, DEEPENDRA

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